

**AMENDMENTS TO THE CLAIMS**

1. (Previously Presented) A video recorder, comprising:

a processor communicating with memory, the memory storing at least one of i) video data of an event and ii) audio data of the event, the video data comprising a series of picture frames;

a loop buffer also storing at least one of the audio data and the video data of the event, the loop buffer also storing at least one of time-delayed audio data and time-delayed video data that precedes the event; and

a set of rules stored in the memory, the set of rules specifying i) at least one of a region of interest and a region of disinterest within a single picture frame, ii) an occurrence that causes transfer of at least one of the time-delayed video data and the time-delayed audio data from the loop buffer to the memory, and iii) a first time and a second time, wherein if the occurrence happens within the at least one of the region of interest and the region of disinterest within a single picture frame at the first time, the set of rules further specifies that at least one of the time-delayed video data and the time-delayed audio data is transferred from the loop buffer to the memory, and if the occurrence happens within the at least one of the region of interest and the region of disinterest within a single picture frame at the second time, the set of rules further specifies that at least one of the time-delayed video data and the time-delayed audio data is not transferred from the loop buffer to the memory.

2. (Previously Presented) A video recorder according to claim 1, wherein the set of rules further specifies a first bitrate associated with the region of interest and a second bitrate associated with the region of disinterest; and

wherein if the occurrence happens within the region of interest, the set of rules further specifies that at least one of the time-delayed video data and the time-delayed audio data is transferred from the loop buffer to the memory at the first bitrate, and if the occurrence happens within the region of disinterest, the set of rules further specifies at least one of the time-delayed video data and the time-delayed audio data is transferred from the loop buffer to the memory at the second bitrate.

3. (Currently Amended) A video recorder according to claim 1, wherein the set of rules further specifies ~~another~~ an other occurrence; and

wherein if the ~~another~~ other occurrence is happening when the occurrence happens within the at least one of the region of interest and the region of disinterest within a single picture frame, then the set of rules further specifies that the ~~another~~ other occurrence is stopped to verify that the occurrence is caused by the ~~another~~ other occurrence.

4. (Previously Presented) A video recorder according to claim 1, wherein the memory stores real-time video data of the event and provides the time-delayed video data, the time-delayed video data preceding the occurrence that causes transfer of at least one of the time-delayed video data and the time-delayed audio data from the loop buffer to the memory.

5. (Previously Presented) A video recorder according to claim 1, wherein the memory stores real-time audio data of the event and provides the time-delayed audio data of the event, the time-delayed audio data preceding the occurrence that causes transfer of at least one of the time-delayed video data and the time-delayed audio data from the loop buffer to the memory.

6. (Currently Amended) A video recorder according to claim 3, wherein the ~~another~~ other occurrence includes operation of a heating, ventilation, and air conditioning system.

7. (Canceled)

8. (Original) A video recorder according to claim 1, wherein the memory comprises a mass-storage device, the mass storage device storing the video data of the event.

9. (Original) A video recorder according to claim 1, wherein the memory comprises an optical storage device.

10. (Original) A video recorder according to claim 1, wherein the memory comprises a memory card.

11. (Original) A video recorder according to claim 1, wherein the memory comprises a flash memory storage device.

12. (Previously Presented) A video recorder according to claim 1, wherein the video recorder interfaces with means for sensing the occurrence and initiates video data of the event.

13. (Previously Presented) A video recorder according to claim 1, wherein the video recorder interfaces with means for sensing the occurrence and initiates audio data of the event.

14. (Original) A video recorder according to claim 1, further comprising an interface to a communications network.

15. (Original) A video recorder according to claim 1, wherein the set of rules tags the video data with metadata, the metadata providing a description of a rule that caused the video data to be stored in the memory.

16. (Previously Presented) A video recorder according to claim 1, wherein the set of rules tags the region of interest with metadata, the metadata providing a description of a rule that caused the video data to be stored in the memory.

17. (Previously Presented) A video recorder according to claim 1, wherein the set of rules tags the region of disinterest with metadata, the metadata providing a description of a rule that caused the video data to be stored in the memory.

18. (Currently Amended) A video recorder, comprising:  
a processor communicating with memory, the memory storing at least one of audio data and video data of an event, the video data comprising a series of picture frames;  
a loop buffer also storing at least one of the audio data and the video data of the event, the loop buffer also storing at least one of time-delayed audio data and time-delayed video data that precedes the event; and

a set of rules stored in the memory, the set of rules specifying i) a first occurrence that causes transfer of at least one of the time-delayed video data and the time-delayed audio data from the loop buffer to the memory and ii) a second occurrence, wherein if the first occurrence happens, then the set of rules further specifies determining whether the second occurrence is also happening, and if the second occurrence is also happening, then the set of rules further specifies that at least one of the time-delayed video data and the time-delayed audio data is not transferred from the loop buffer to the memory.

19. (Original) A video recorder according to claim 18, further comprising an interface to a communications network, the interface allowing the video recorder to transfer the audio data and the video data to a remote location via the communications network.

20. (Original) A video recorder according to claim 18, further comprising a user interface for configuring the video recorder.

21. (Previously Presented) A video recorder according to claim 18, wherein if the second occurrence is happening when the first occurrence happens, then the set of rules further specifies that the second occurrence is stopped to verify that the first occurrence is caused by the second occurrence.